IN THE UNITED STATES PATENT & TRADEMARK OFFICE

Applicant	:	V. K. JaiPrakash, et al.)	Examiner: TBA Art Unit Unknown
Serial No.	:	TBA)	
Filed	:	Filed Herewith)	
For	:	DRAM VERTICAL TRANSISTOR AND TRENCHCAPACITOR MEMORY CELLS AND METHOD OF FABRICATION)))	

PRELIMINARY AMENDMENT

Mail Stop Patent Application Commissioner For Patents P. O. Box 1450 Alexandria, VA 22313-1450

Dear Sir:

Preliminary to examination of the above-referenced patent application, please make the following amendments.